

Engineering Design Team (EDT) - Certificate of Volatility

Model: PCI C Da (all variations & revisions)	Part Number: 019-01854, 019-01960, 019-02039, 019-02216, 019-02219, 019-02234, 019-11854, 019-12219, 019-14879	Address: Engineering Design Team, Inc. 3423 NE John Olsen Avenue Hillsboro, OR 97124 U.S.A. +1-503-690-1234 or 1-800-435-4320
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Volatile Memory

Does the device contain volatile memory (memory whose contents are lost when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (SRAM, DRAM, etc): FPGA: Xilinx XC2S200-6FG456C	Size: 5292 logic cells. 56Kbit Block RAM.	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: DMA logic, RAM used as FIFOs for buffering. Commonly referred to as the "PCI FPGA".	Steps to clear memory: Power down
Type (SRAM, DRAM, etc): FPGA: Xilinx XC2S300E-6FG456C or XC2S600E-6FGG456C	Size: 6912 or 15552 logic cells. 64K or 288K bits Block RAM.	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Data processing logic. Commonly referred to as the "UI FPGA".	Steps to clear memory: Power down
Type (SRAM, DRAM, etc): SRAM: IDT 71V65603S100PFG	Size: 9Mbit	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function:	Steps to clear memory: Power down

Non-Volatile Memory

Does the device contain non-volatile memory (memory whose contents are retained when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (Flash, EEPROM, etc): CPLD: Xilinx XC9572XL-10VQG64C	Size: 72 macrocells	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: Configuration control: configures the "PCI FPGA" from flash on board power up	Steps to clear memory: JTAG. Contact EDT.
Type (Flash, EEPROM, etc): NOR Flash: Cypress AM29LV081B-70ED	Size: 8Mbit	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Configuration memory: stores "PCI FPGA" configuration bitfiles	Steps to clear memory: EDT's pload program. Contact EDT.

Mass Storage

Does the device contain mass storage memory (Hard Disk Drive, Tape Backup)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (HDD, Tape, etc):	Size:	User Modifiable: <input type="checkbox"/> Yes <input type="checkbox"/> No	Function:	Steps to clear memory:
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USB
Does the item accept USB input and if so, for what purpose (i.e. Print Jobs, device firmware updates, scan upload)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below
Can any data other than scan upload be sent to the USB device)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below

RF/Rfid
Does the item use RF or Rfid for receive or transmit of any data including remote diagnostics (e.g. Cellular phone, Bluetooth)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below
Purpose:
Frequency: Bandwidth:
Modulation: Effective Radiate Power (ERP):
Specifications:

Other Transmission Capabilities
Does the device employ any other methods of non-wired access to transmit or receive any data whatsoever (e.g. anything other than standard hard wired TCP/IP, direct USB, or parallel connections)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe below
Frequency: Bandwidth:
Modulation: Effective Radiate Power (ERP):
Specifications:

Other Capabilities
Does the device employ any other method of communications such as a Modem to transmit or receive any data whatsoever? <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No If yes, describe below:
The device's purpose is to receive and/or transmit serial or parallel digital data across using the LVDS or RS422 signaling standards, DMA'ing to and/or from the host computer.
Specifications: With one DMA channel: up to 210 Mbytes/s. Sixteen DMA channels: up to 70Mbits/s/channel.

Author Information			
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