

Engineering Design Team (EDT) - Certificate of Volatility

Model: VisionLink F1, all variations VisionLink F4, all variations	Part Numbers: 019-14836-0X, 019-14836-1X, 019-14852-0X, 019-14852-1X, 019-14853-0X, 019-14853-1X, 019-14854-0X, 019-14854-1X, 019-14856-0X, 019-14856-1X, 019-14857-0X, 019-14857-1X, 019-15005-0X, 019-15005-1X, 019-15100-0X, 019-15100-1X, 019-15104-0X, 019-15104-1X, 019-15292-0X, 019-15292-1X, 019-15367-0X, 019-15367-1X	Address: Engineering Design Team, Inc. 3423 NE John Olsen Avenue Hillsboro, OR 97124 U.S.A. +1-503-690-1234 or 1-800-435-4320
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Volatile Memory

Does the device contain volatile memory (memory whose contents are lost when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (SRAM, DRAM, etc): MT41K64M16TW-107:J DDR3 SDRAM	Size: 1MBit	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: FIFO	Steps to clear memory: Power off board
Type (SRAM, DRAM, etc): 5CGTFD5C5U19C7N FPGA	Size: N/A	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Framegrabber	Steps to clear memory: Power off board
Type (SRAM, DRAM, etc): MSP430G2553 microcontroller	Size: 512B	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: Status LED, IRIG-B, POCL	Steps to clear memory: Power off board

Non-Volatile Memory

Does the device contain non-volatile memory (memory whose contents are retained when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (Flash, EEPROM, etc): MSP430G2553 microcontroller	Size: 16 KB	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: Status LED, IRIG-B, POCL	Steps to clear memory: Contact EDT
Type (Flash, EEPROM, etc): SPI Flash N25Q064A13ESE40G OR MT25QL128ABA1ESE	Size: 64Mbit OR 128 Mbit	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Configuration Memory	Steps to clear memory: Contact EDT

Mass Storage

Does the device contain mass storage memory (Hard Disk Drive, Tape Backup)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (HDD, Tape, etc):	Size:	User Modifiable: <input type="checkbox"/> Yes <input type="checkbox"/> No	Function:	Steps to clear memory:
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USB
Does the item accept USB input and if so, for what purpose (i.e. Print Jobs, device firmware updates, scan upload)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below
Can any data other than scan upload be sent to the USB device)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below

RF/Rfid
Does the item use RF or RFID for receive or transmit of any data including remote diagnostics (e.g. Cellular phone, Bluetooth)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe the type, size, function, and steps to clear the memory below
Purpose:
Frequency: Bandwidth:
Modulation: Effective Radiate Power (ERP):
Specifications:

Other Transmission Capabilities
Does the device employ any other methods of non-wired access to transmit or receive any data whatsoever (e.g. anything other than standard hard wired TCP/IP, direct USB, or parallel connections)? <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No If yes, describe below
Frequency: Bandwidth:
Modulation: Effective Radiate Power (ERP):
Specifications:

Other Capabilities
Does the device employ any other method of communications such as a Modem to transmit or receive any data whatsoever? <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No If yes, describe below:
Device's primary purpose is to receive Camera Link format image data and transfer to computer memory via Direct Memory Access. Additionally, device is capable of transmitting and receiving command and control signals on the Camera Link connector CC lines, IRIG-B data on the IRIG-B connector (subset of part numbers with IRIG capability), and trigger signals (input and output) via the optocoupler pins.
Specifications:

Author Information			
Name	Title	Email	Department
Jeff Morgan	Hardware Engineer	jeff@edt.com	Eng
Date Prepared:			

2/6/2019

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Volatile Memory

Does the device contain volatile memory (memory whose contents are lost when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (SRAM, DRAM, etc): 5CGTFD5C5U19I7N FPGA	Size: N/A	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Framegrabber	Steps to clear memory: Power off board
Type (SRAM, DRAM, etc): MSP430F2272IRHAT microcontroller	Size: 1KB	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: IRIG-B	Steps to clear memory: Power off board
Type (SRAM, DRAM, etc): MSP430G2553 microcontroller	Size: 512B	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: Status LED, POCL	Steps to clear memory: Power off board

Non-Volatile Memory

Does the device contain non-volatile memory (memory whose contents are retained when power is removed)?

Yes No If yes, describe the type, size, function, and steps to clear the memory below

Type (Flash, EEPROM, etc): MSP430F2272IRHAT microcontroller	Size: 32 KB	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: IRIG-B	Steps to clear memory: Contact EDT
Type (Flash, EEPROM, etc): MSP430G2553 microcontroller	Size: 16 KB	User Modifiable: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Function: Status LED, POCL	Steps to clear memory: Contact EDT
Type (Flash, EEPROM, etc): SPI Flash N25Q064A13ESE40G OR MT25QL128ABA1ESE	Size: 64Mbit OR 128 Mbit	User Modifiable: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Function: Configuration Memory	Steps to clear memory: Contact EDT

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Specifications:	

Author Information			
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Date Prepared:			

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