16TE3

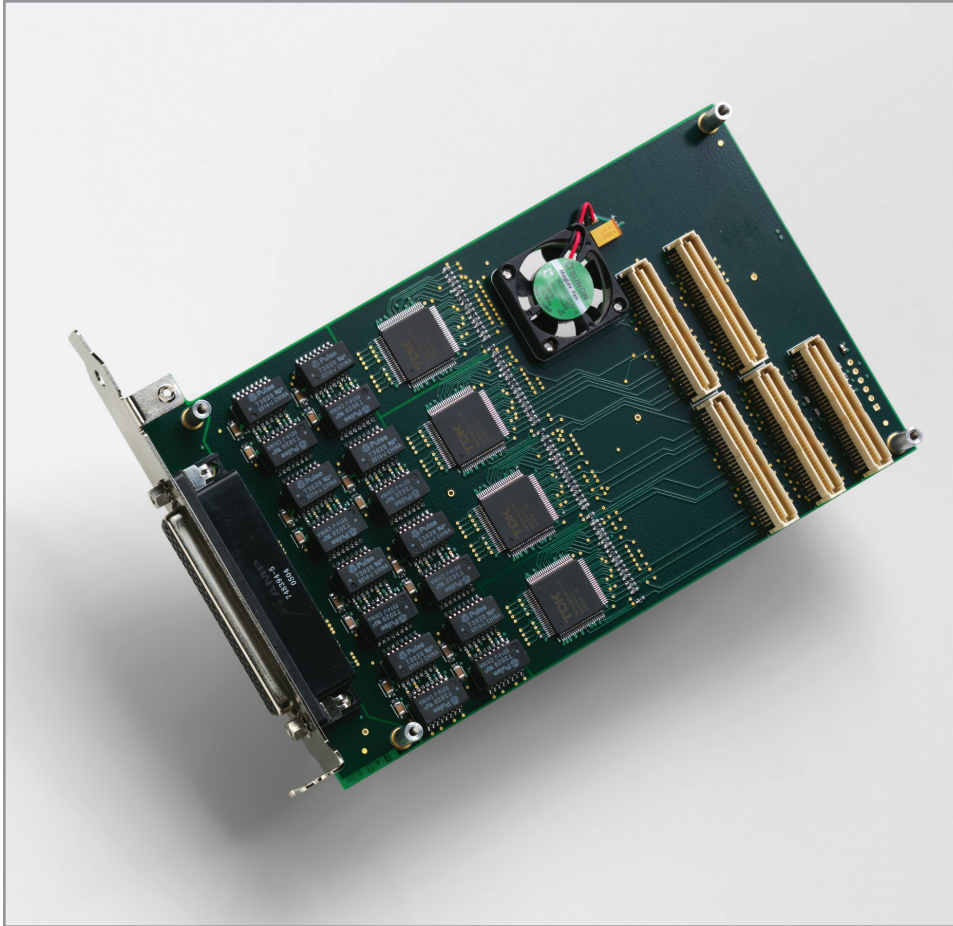**Sixteen-connection interface for E3 (T3)**

Description

The 16TE3 is a mezzanine board that pairs with a PCI / PCIe main board to provide fast data transfer. It supports sixteen independent full-duplex line interfaces for E3/T3.

EDT provides standard FPGA configuration files, and custom configuration files can be requested.

The main board supplies DMA, plus additional memory and programmable FPGA resources.

Applications

Communications monitoring (serial data)

Signal generation (E3 or T3)

Features

Mezzanine board – pairs with an EDT main board (PCI or PCIe), which adds DMA, programmable FPGA resources, and memory

I/O: E3/T3 – sixteen independent full-duplex line interfaces

Specifications

Product Type	Sixteen-connection interface for E3 (T3); it requires an EDT PCI / PCIe main board.	
FPGAs and Memory	Programmable FPGA and memory resources are provided by the main board.	
Data Rates	Dependent on such factors as data format, main board, and system variables.	
Data Format (I/O)	**E3/T3**	
	Independent I/Os	16 (each full duplex)
	Transformer coupled	Yes
	Compliance	G.703, T1.102
	Mb/s	Up to 51.84 (with 75-ohm coaxial cable over 1100 feet)
	Coding	HDB3 / B3ZS
	Jitter attenuation	Yes
Connectors	One 62-pin AMP (mate to AMP 748367-1)	
Cabling	Consult EDT for purchase options.	
Physical	Weight	3.7 oz. typical
	Dimensions	6.6 x 4.2 x 0.75 in. (with a main board)
Environmental	Temperature (operating / non-operating)	0° to 40° C / -40° to 70° C
	Humidity (operating / non-operating)	1% to 90%, non-condensing at 40° C / 95%, non-condensing at 45° C
System and Software	For details on system requirements and EDT-provided software driver packages, see specifications for your EDT main board.	

Ordering Options

- Main board: PCI SS / GS or PCIe8 LX / FX / SX

For more options, see main board detail.
Ask about custom options.