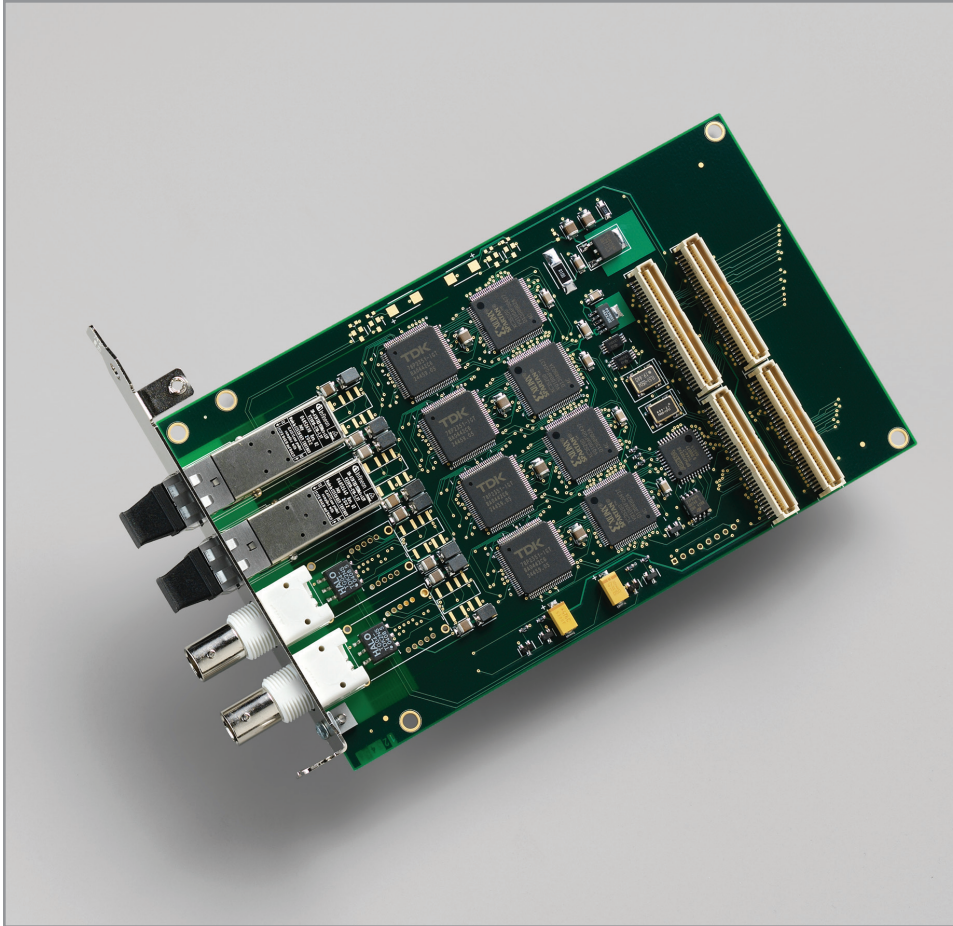


HRC

High-rate communication interface for E4 / STS3 / OC3 (STM1)



Description

The HRC is a mezzanine board that pairs with a PCI main board to provide fast data transfer. It accepts both electrical signals (E4 or STS3) and optical signals (OC3/STM1) in various combinations.

The board has four connector slots; each supports either an electrical BNC 75-ohm CMI coaxial interface, or an optical LC 1310 nm NRZ single-mode transceiver. The BNCs can be set by programmable control as either input or output; if bidirectional operation is required, a maximum of two ports will be usable.

EDT provides FPGA configuration files to support framing and descrambling for OC3/STM1 framed data. Custom configuration files can be requested.

The main board supplies DMA, plus additional memory and programmable FPGA resources.

Features

Mezzanine board – pairs with an EDT main board (PCI or PCIe), which adds DMA, programmable FPGA resources, and memory

Ports 0, 1, 2, 3 each support electrical (E4 or STS3) or optical (OC3/STM1):

- Electrical via unidirectional BNCs (75-ohm CMI coaxial G.703)
- Optical via bidirectional LCs (1310 nm NRZ single-mode SFF)

Applications

Telecommunications network monitoring

Telecommunications data recording

