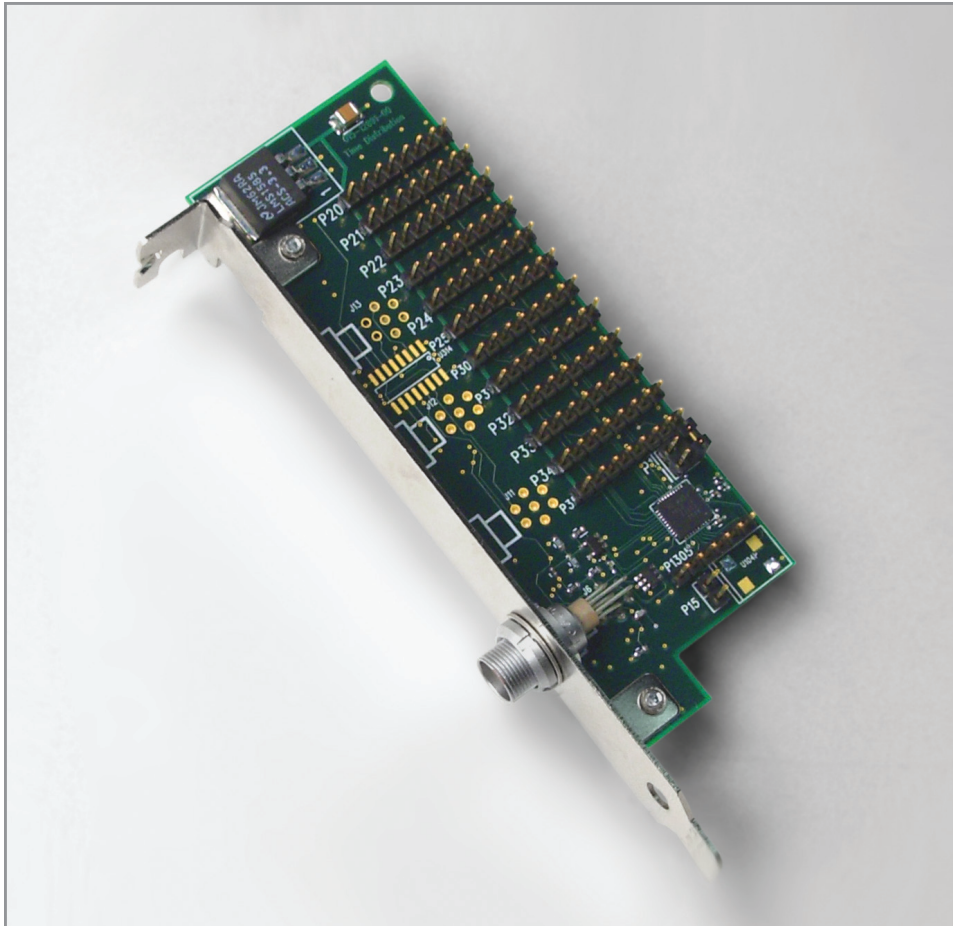


Time Distribution

Time acquisition and distribution to multiple interfaces



Description

The Time Distribution board is an auxiliary board that receives a time code signal (from a 1 pps or IRIG-B time code source) and distributes it to as many as twelve EDT main boards to timestamp the data.

Each of the headers on the Time Distribution board provides a connection, via ribbon cable, to an EDT main board to receive the time signal and to control the signal source.

Ordering Options

- Main board: PCI GS or PCIe8 LX / FX / SX
- Mezzanine board: Any
- Connector: DB9 (EDT part #016-03005-00) or BNC (EDT part #016-02894-00)
- Cabling: Up to 12 ribbon cables

Bold is default. For more options, see mezzanine board detail. **Ask** about custom options.

Specifications

| | | |
|----------------------------------|---|---|
| Product Type | Auxiliary board that acquires and distributes time code data; it requires a main board. | |
| Microcontroller Resources | One microcontroller (TI MSP430F2272 ultra-low power – see details at www.msp430.com) with these resources: RAM 1 KB Flash 32 KB Clock 16 MHz, programmable to 1, 8, or 12 MHz | |
| Data Rates | Dependent on such factors as data format, main and mezzanine boards, and system variables: SPI serial bus 10,000 B/s, full duplex, programmable | |
| Data Format (I/O) | Time code (from external receiver) | 1 pps, IRIG-B, or other input, with user-configurable output |
| Connectors and Cabling | Consult EDT for purchase options: To 7-pin Lemo on board, from time code source To/from main board | Via one DB9 (for 1 pps or IRIG-B), EDT part #016-03005-00 or one BNC (for IRIG-B only), EDT part #016-02894-00 Via twelve 8-position Bergs (up to twelve ribbon cables, with two included) |
| Physical | Weight Dimensions | 1.5 oz. typical 4.2 x 1.5 x 0.75 in. (6.6 x 4.2 x 0.75 in. with a main board) |
| Environmental | Temperature (operating / non-operating) Humidity (operating / non-operating) | 0° to 40° C / -40° to 70° C 1% to 90%, non-condensing at 40° C / 95%, non-condensing at 45° C |
| System and Software | System requirements and EDT-provided software driver packages are discussed in the specifications for your EDT main board. | |